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**Proceedings Paper:**

Plump, Detlef [orcid.org/0000-0002-1148-822X](https://orcid.org/0000-0002-1148-822X) and Wulandari, Gia (2018) Verifying a Copying Garbage Collector in GP 2. In: Mazzara, Manuel, Salaün, Gwen and Ober, Iulian, (eds.) Software Technologies: Applications and Foundations - STAF 2018 Collocated Workshops, Revised Selected Papers. 9th International Workshop on Graph Computation Models (GCM 2018), 27 Jun 2018 Lecture Notes in Computer Science (including subseries Lecture Notes in Artificial Intelligence and Lecture Notes in Bioinformatics) . Springer , FRA , pp. 479-494.

[https://doi.org/10.1007/978-3-030-04771-9\\_34](https://doi.org/10.1007/978-3-030-04771-9_34)

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# Verifying a Copying Garbage Collector in GP 2

Gia S. Wulandari<sup>\*1,2</sup> and Detlef Plump<sup>1</sup>

<sup>1</sup> Department of Computer Science, University of York, UK

<sup>2</sup> School of Computing, Telkom University, Indonesia

**Abstract.** Cheney’s copying garbage collector is regarded as a challenging test case for formal approaches to the verification of imperative programs with pointers. The algorithm works for possibly cyclic data structures with unrestricted sharing which cannot be handled by standard separation logics. In addition, the algorithm relocates data and requires establishing an isomorphism between the initial and the final data structure of a program run.

We present an implementation of Cheney’s garbage collector in the graph programming language GP 2 and a proof that it is totally correct. Our proof is shorter and less complicated than comparable proofs in the literature. This is partly due to the fact that the GP 2 program abstracts from details of memory management such as address arithmetic. We use sound proof rules previously employed in the verification of GP 2 programs but treat assertions semantically because current assertion languages for graph transformation cannot express the existence of an isomorphism between initial and final graphs.

## 1 Introduction

Poskitt and Plump developed Hoare-style proof systems for verifying the partial and total correctness of graph programs and showed that their proof calculi are sound with respect to the operational semantics of graph programs in the language GP 2 [13, 12]. In these calculi, pre- and postconditions of programs are so-called E-conditions which extend nested graph conditions with support for expressions. E-conditions are limited to specify first-order graph properties and cannot express non-local properties such as connectedness or the existence of arbitrary-length paths. M-conditions [15] overcome this limitation in that they express monadic second-order properties of graphs.

In this paper, we present the verification of a graph program that cannot be proved correct by using E- or M-conditions because its correctness requires to establish a certain isomorphism between input and output graphs. We implement Cheney’s copying garbage collector [3] in the graph programming language GP 2 and prove that it is totally correct. Cheney’s algorithm is regarded as a challenge for formal approaches to verifying pointer programs. This is because it works for possibly cyclic data structures with unrestricted sharing which cannot be handled by standard separation logics [16, 5]. In addition, the algorithm relocates

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\* Supported by Indonesia Endowment Fund for Education (LPDP)

data which requires establishing an isomorphism between the initial and the final data structure of a program run.

While cycles and unrestricted sharing are not a problem for formal assertions based on nested graph conditions, the existence of an isomorphism between initial and final graphs of program runs cannot be expressed with such assertions. Therefore we treat assertions semantically, without a formal language, but use sound proof rules that were previously employed in GP 2 verification.

The remainder of this paper is structured as follows: Section 2 briefly describes the graph programming language GP 2, followed by Section 3, where we introduce the basic notions of graph program verification. In Section 4, we present an implementation of Cheney’s copying garbage collector in GP 2. In Section 5, we precisely specify the garbage collector by pre- and postconditions. In Section 6, we prove that our implementation is partially correct, will terminate and cannot fail. In Section 7, we argue that our proof of partial correctness is shorter and less complicated than comparable proofs in the literature. Finally, we conclude and give some topics for future work in Section 8.

## 2 Graph Programs

We briefly describe a subset of the graph programming language GP 2 that is sufficient for our case study. A full definition of GP 2, including a formal operational semantics, can be found in [11]. The language is implemented by a compiler generating C code [1].

The principal programming constructs in GP 2 are graph-transformation rules labelled with expressions. For example, the program **cheney** in Fig. 4 contains the declarations of five rules. Rules operate on *host graphs* whose nodes and edges are labelled with heterogeneous lists of integers and character strings. Variables in rules are typed, where **list** is the most general type. In particular, integers and strings are considered as lists of length one. By abuse of terminology, we call items *unlabelled* if they are labelled with the empty list.

Besides a list label, nodes and edges may carry a *mark* which is one of the values **green**, **blue**, **grey** and **dashed** (where **grey** and **dashed** are reserved for nodes and edges, respectively). Marks are convenient to highlight items in input or output graphs, and to record which items have been visited during a graph traversal. For convenience, we sometimes refer to unmarked nodes as *white* nodes.

Moreover, nodes in rules and host graphs may be distinguished as *roots*. For example, in the rule **copy\_root** of Fig. 4, nodes with a thick black border are roots. While roots are normally used to restrict the set of rule matches [1], we use them in this paper to specify reachable subgraphs.

The grammar in Fig. 1 gives the abstract syntax of graph programs in our subset (without the syntax of rule declarations). A program consists of a number of rule declarations and exactly one declaration of a main command sequence. The category RuleId refers to declarations of rules in RuleDecl. The call of a

rule set  $\{r_1, \dots, r_n\}$  non-deterministically applies one of the rules whose left-hand graph matches a subgraph of the host graph. Rule matching is injective and involves instantiating the variables in rules with host graph labels. The call *fails* if none of the rules is applicable to the host graph. A loop command  $\mathcal{R}!$  applies the rule set  $\mathcal{R}$  repeatedly until it fails. When this is the case,  $\mathcal{R}!$  terminates with the graph resulting from the last successful application of  $\mathcal{R}$ .

```

Prog      ::= Decl {Decl}
Decl      ::= RuleDecl | MainDecl
MainDecl  ::= Main '=' ComSeq
ComSeq    ::= Com {';' Com}
Com       ::= RuleSetCall ['!']
RuleSetCall ::= RuleId | '{' RuleId {';' RuleId} '}'

```

**Fig. 1.** Abstract syntax of a subset of GP 2 programs

The semantics of graph programs is given in the style of structural operational semantics [9]. The meaning of a graph program  $P$  is the function  $\llbracket P \rrbracket$  mapping an input graph  $G$  to the set  $\llbracket P \rrbracket G$  of all possible outcomes of executing  $P$  on  $G$ . Possible outcomes include the value *fail* which indicates a failed program run, and the value  $\perp$  which indicates divergence. We say that program  $P$  *can diverge from* graph  $G$  if there exists an infinite program run starting from  $G$ .

Writing  $\mathcal{G}^\oplus$  for the set of all host graphs extended with the values *fail* and  $\perp$ , the semantic function  $\llbracket \_ \rrbracket : \text{ComSeq} \rightarrow (\mathcal{G} \rightarrow 2^{\mathcal{G}^\oplus})$  is defined by

$$\llbracket P \rrbracket G = \{X \in (\mathcal{G} \cup \{\text{fail}\}) \mid \langle P, G \rangle \xrightarrow{+} X\} \cup \{\perp \mid P \text{ can diverge from } G\}$$

where  $\rightarrow$  is a small-step transition relation on *configurations* which is inductively defined by inference rules. In our setting, a configuration is either a command sequence together with a host graph, just a host graph or the special element *fail*:  $\rightarrow \subseteq (\text{ComSeq} \times \mathcal{G}) \times ((\text{ComSeq} \times \mathcal{G}) \cup \mathcal{G} \cup \{\text{fail}\})$ .

Configurations in  $\text{ComSeq} \times \mathcal{G}$ , given by a rest program and a host graph, represent states of unfinished computations while graphs in  $\mathcal{G}$  are final states or *results* of computations.

Figure 2 shows the inference rules for the GP 2 commands used in this paper. The rules contain meta-variables for command sequences and graphs, where  $R$  stands for a call in category *RuleSetCall* (as defined by the grammar in Figure 1),  $P$  and  $Q$  stand for command sequences in category *ComSeq*, and  $G, H$  stand for graphs in  $\mathcal{G}$ . The transitive closure of  $\rightarrow$  is denoted by  $\rightarrow^+$ . We write  $G \Rightarrow_R H$  if  $H$  results from host graph  $G$  by applying the rule set  $R$ , while  $G \not\Rightarrow_R H$  means that there is no graph  $H$  such that  $G \Rightarrow_R H$  (application of  $R$  fails).

### 3 Verification of Graph Programs

As mentioned in the Introduction, we treat assertions semantically and express pre- and postconditions in ordinary mathematical language (similar to [10]). As is usual in Hoare logic, we use triples  $\{c\} P \{d\}$  to state that program  $P$  is

$$\begin{array}{c}
\text{[call}_1\text{]} \frac{G \Rightarrow_R H}{\langle R, G \rangle \rightarrow H} \quad \text{[call}_2\text{]} \frac{G \not\Rightarrow_R}{\langle R, G \rangle \rightarrow \text{fail}} \quad \text{[seq}_1\text{]} \frac{\langle P, G \rangle \rightarrow \langle P', H \rangle}{\langle P; Q, G \rangle \rightarrow \langle P'; Q, H \rangle} \\
\text{[seq}_2\text{]} \frac{\langle P, G \rangle \rightarrow H}{\langle P; Q, G \rangle \rightarrow \langle Q, H \rangle} \quad \text{[seq}_3\text{]} \frac{\langle P, G \rangle \rightarrow \text{fail}}{\langle P; Q, G \rangle \rightarrow \text{fail}} \quad \text{[alap}_1\text{]} \frac{\langle P, G \rangle \rightarrow^+ H}{\langle P!, G \rangle \rightarrow \langle P!, H \rangle} \\
\text{[alap}_2\text{]} \frac{\langle P, G \rangle \rightarrow^+ \text{fail}}{\langle P!, G \rangle \rightarrow G}
\end{array}$$

**Fig. 2.** Semantic inference rules

partially correct with respect to precondition  $c$  and postcondition  $d$ . Intuitively, this means that for every graph  $G$  satisfying  $c$ , any graph  $H$  resulting from executing  $P$  on  $G$  will satisfy  $d$ .

Given a graph  $G$  and some assertion  $c$ , we write  $G \models c$  if  $G$  satisfies  $c$ . If, in addition to partial correctness,  $P$  cannot diverge or fail from graphs satisfying  $c$ , the program is totally correct.

**Definition 1 (Partial and total correctness [14]).** *A graph program  $P$  is partially correct with respect to a precondition  $c$  and a postcondition  $d$ , if for every host graph  $G$  and every graph  $H$  in  $\llbracket P \rrbracket G$ ,  $G \models c$  implies  $H \models d$ .*

*$P$  is totally correct with respect  $c$  and  $d$  if it is partially correct and for every host graph  $G$  such that  $G \models c$ ,  $\llbracket P \rrbracket G \cap \{\perp, \text{fail}\} = \emptyset$ .*

We write  $\models \{c\} P \{d\}$  if  $P$  is partially correct with respect to  $c$  and  $d$ . In Hoare logic, proof rules in the form of axioms and inference rules are used to construct proof trees decorated with Hoare triples. Proof trees are defined in Definition 2. The rules we use in this paper are shown in Fig. 3; they are taken from [12, 13, 15] except for [rule app], which replaces an axiom involving the weakest liberal precondition. As our semantic assertions do not come with an algorithm for calculating the weakest liberal precondition, determining and proving this condition would unnecessarily inflate our proofs.

**Definition 2 (Proof tree [12]).** *If  $\{c\} P \{d\}$  is an instance of an axiom  $X$  then (a) is a proof tree. If  $\{c\} P \{d\}$  can be instantiated from the conclusion of an inference rule  $X$ , and there are proof trees  $T_1, \dots, T_n$  with conclusions that are instances of the  $n$  premises of  $X$ , then (b) is a proof tree.*

$$\begin{array}{ll}
\text{(a)} & X \frac{}{\{c\} P \{d\}} \\
\text{(b)} & X \frac{T_1 \quad \dots \quad T_n}{\{c\} P \{d\}}
\end{array}$$

Property  $\text{App}(\mathcal{R})$  in rule [alap] expresses that the rule set  $\mathcal{R}$  is applicable. By the semantics of the as-long-as-possible command,  $\mathcal{R}$  is not applicable to any graph resulting from the loop  $\mathcal{R}!$

**Definition 3 ( $\text{App}(\mathcal{R})$ ).** *Let  $\mathcal{R}$  be a set of rules. A graph  $G$  satisfies  $\text{App}(\mathcal{R})$  if and only if there exists a graph  $H$  such that  $G \Rightarrow_{\mathcal{R}} H$ .*

The proof rules we use in this paper are known to be sound with respect to GP 2's operational semantics.

$$\begin{array}{ll}
[\text{ruleapp}] \frac{\models \{c\} r \{d\}}{\{c\} r \{d\}} & [\text{ruleset}] \frac{\{c\} r \{d\} \text{ for all } r \in \mathcal{R}}{\{c\} \mathcal{R} \{d\}} \\
[\text{alap}] \frac{\{inv\} \mathcal{R} \{inv\}}{\{inv\} \mathcal{R}! \{\neg \text{App}(\mathcal{R}) \wedge inv\}} & [\text{comp}] \frac{\{c\} P \{d\} \quad \{d\} Q \{e\}}{\{c\} P; Q \{e\}} \\
[\text{cons}] \frac{c \Rightarrow c' \quad \{c'\} P \{d'\} \quad d' \Rightarrow d}{\{c\} P \{d\}}
\end{array}$$

**Fig. 3.** Proof rules for graph program verification

**Theorem 1 (Soundness of proof rules [12]).** *Given a program  $P$  and assertions  $c$  and  $d$ ,  $\vdash \{c\} P \{d\}$  implies  $\models \{c\} P \{d\}$ .*

Here  $\vdash \{c\} P \{d\}$  means that there exists a proof tree with root  $\{c\} P \{d\}$ .

## 4 Cheney's Copying Garbage Collector in GP2

Cheney's garbage collector assumes two disjoint, equally large regions of memory where the first region holds the data structure to be garbage collected and the second region consists of free cells. The structure that is reachable from the root cell in the first area is copied to the second region. Subsequently, the complete first region can be freed by the memory management system. Adopting this technique, we construct the graph program **cheney** in Fig. 4 to garbage collect an input graph. We model the free cells assumed by Cheney's method as unlabelled isolated nodes. This is similar to the store model used by [6] for pointer verification.

As input, our program assumes a graph that can be partitioned into two subgraphs: the graph to be garbage collected, and the graph that models a region of free memory cells. We differentiate the two regions by colours. White and grey nodes are used for the first region while green and blue nodes are used for the second region. The root cell in the first region is a unique root node. Hence, garbage collection involves identifying the subgraph reachable from the root node and copying it to the unlabelled subgraph. In Fig. 5, we give an example of the execution of **cheney**.

As in [5, 16], we do not model the subsequent freeing of cells in the first region. This would be easy to achieve by a few rules which change all white and grey nodes into unlabelled green nodes and delete all edges between these nodes.

## 5 Case Study: Specification

We assume that the input graph of the program **cheney** can be partitioned into two subgraphs as described above. This partition will persist during program execution. Given an input graph  $G$ , we use  $\text{Old}_G$  and  $\text{New}_G$  to refer to these subgraphs, where  $\text{Old}_G$  is to be garbage collected.

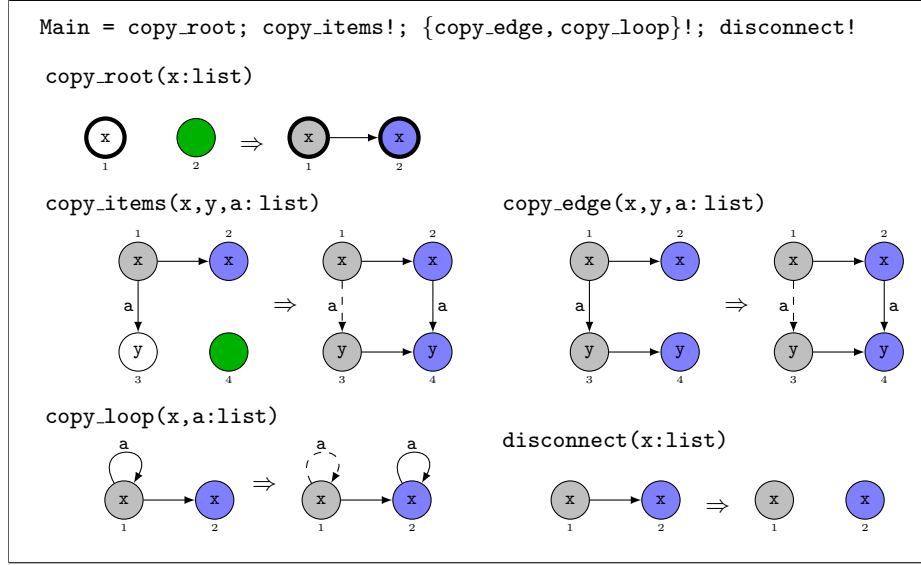


Fig. 4. Graph program **cheney**

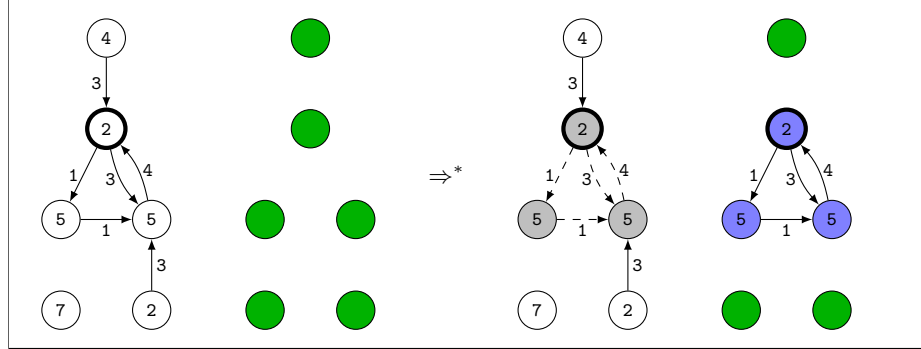


Fig. 5. A graph before and after the execution of **cheney**

**Definition 4** ( $\text{Old}_G$  and  $\text{New}_G$ ). Given a graph  $G$ , we denote by  $\text{Old}_G$  the subgraph consisting of all unmarked and grey nodes and all edges between them. Also,  $\text{New}_G$  denotes the subgraph consisting of all green and blue nodes and all edges between them.

The aim of program **cheney** is to copy to  $\text{New}_G$  the subgraph of  $\text{Old}_G$  consisting of all nodes and edges that are reachable from the root. We denote this subgraph by  $\text{Reach}(\text{Old}_G)$ .

**Definition 5** ( $\text{Reach}(G)$ ). *Given a graph  $G$  with a unique root node  $v$ , we denote by  $\text{Reach}(G)$  the subgraph of  $G$  consisting all nodes and edges reachable from  $v$  by directed paths.*

As the garbage collector copies a subgraph of  $\text{Old}_G$  to  $\text{New}_G$ , its formal specification by pre- and postcondition needs to require an isomorphism between  $\text{Reach}(\text{Old})$  in the input graph and  $\text{Reach}(\text{New})$  in the result graph. However, program **cheney** uses marks to distinguish Old and New and reachable subgraphs. Therefore, we introduce graph morphisms that preserve labels, sources, targets, and roots, but ignore marks.

**Definition 6 (Liberal graph morphism).** *Given graphs  $G$  and  $H$ , a liberal graph morphism  $f: G \rightarrow H$  is a pair of mappings  $f = \langle f_V: V_G \rightarrow V_H, f_E: E_G \rightarrow E_H \rangle$  that preserve sources, targets, labels and roots.*

Then, an isomorphism is a bijective liberal morphism that reflects root nodes.

**Definition 7 (Isomorphism).** *A liberal graph morphism  $f: G \rightarrow H$  is an isomorphism if  $f_V$  and  $f_E$  are bijective and if for each node  $v$  in  $G$ ,  $v$  is a root if and only if  $f_V(v)$  is a root.*

Given an input graph **Start**, program **cheney** has to produce a graph **Result** such that  $\text{Reach}(\text{Old}_{\text{Start}})$  is isomorphic to  $\text{Reach}(\text{New}_{\text{Result}})$ . We specify the pre- and postcondition of **cheney** as follows.

**Precondition.** Each node in **Start** is either unmarked or green, and the number of unmarked and green nodes is the same. All edges are unmarked. All green nodes are isolated and unlabelled. There is a unique root node which is unmarked.

**Postcondition.** Each node in **Result** is either in  $\text{Old}_{\text{Result}}$  or  $\text{New}_{\text{Result}}$ , and the number of nodes in  $\text{Old}_{\text{Result}}$  and  $\text{New}_{\text{Result}}$  is the same. There are no edges connecting  $\text{New}_{\text{Result}}$  and  $\text{Old}_{\text{Result}}$ . There is a unique grey root node in  $\text{Old}_{\text{Result}}$  and a unique blue root node in  $\text{New}_{\text{Result}}$ . In  $\text{Reach}(\text{Old}_{\text{Result}})$ , the nodes are grey and the edges are dashed, while other items in  $\text{Old}_{\text{Result}}$  are unmarked. In  $\text{Reach}(\text{New}_{\text{Result}})$ , the nodes are blue and the edges are unmarked, while all other nodes in  $\text{New}_{\text{Result}}$  are isolated green nodes which are unlabelled. Moreover,  $\text{Reach}(\text{Old}_{\text{Start}})$  and  $\text{Reach}(\text{New}_{\text{Result}})$  are isomorphic.

## 6 Case Study: Proof

### 6.1 Partial Correctness

To prove that program **cheney** is correct with respect to its pre- and postcondition, we consider an arbitrary execution of **cheney** that transforms an input graph **Start** satisfying the precondition into a result graph **Result**. Our proof rests on a property  $\text{inv}_{\text{Start}}$ , defined in Definition 8, which holds for **Start** and



is an invariant for all five rules of **cheney** (shown in Lemma 2). Thus  $\text{inv}_{\text{Start}}$  holds for each graph in the execution sequence  $\text{Start} \Rightarrow^* \text{Result}$ .

In particular, for each graph  $G$  such that  $\text{Start} \Rightarrow^* G \Rightarrow^* \text{Result}$ ,  $\text{inv}_{\text{Start}}$  asserts the existence of two isomorphisms: one between  $\text{Old}_{\text{Start}}$  and  $\text{Old}_G$  and another one between  $\text{Grey}_G$  and  $\text{Blue}_G$ . Here  $\text{Grey}_G$  is the subgraph of  $G$  consisting all grey nodes and all dashed edges between them, and  $\text{Blue}_G$  is the subgraph of  $G$  consisting all blue nodes and all edges between them. We then establish that  $\text{Grey}_{\text{Result}}$  equals  $\text{Reach}(\text{Old}_{\text{Result}})$  while  $\text{Blue}_{\text{Result}}$  equals  $\text{Reach}(\text{New}_{\text{Result}})$ .

Roughly,  $\text{Old}_{\text{Start}}$  and  $\text{Old}_G$  are isomorphic because (1) no rule deletes or creates nodes, (2) no rule deletes, creates or relabels edges within  $\text{Old}$  (any edge created or deleted is incident to a blue node), (3) no rule can change the colour or label of a grey node, and (4) rules can change unmarked nodes only by turning them grey (while preserving the label).

Moreover,  $\text{Grey}_G$  and  $\text{Blue}_G$  are isomorphic because (1)  $\text{Grey}_{\text{Start}} = \emptyset = \text{Blue}_{\text{Start}}$ , (2) **copy\_root** creates one-node in graphs Grey and Blue whose nodes are roots with the same label, and an unlabelled edge connecting Grey and Blue, called an *isomorphism edge*, which represents the node mapping of the isomorphism, (3) **copy\_items** extends both Grey and Blue by one edge and its target node, where the edges have the same label and sources connected by an isomorphism edge, and creates an isomorphism edge between the target nodes, (4) **copy\_edge** extends Grey and Blue by one edge each, where the edges have the same label and have their sources resp. targets connected by an isomorphism edge, (5) **copy\_loop** works similar to **copy\_edge** except that the new edges are loops, and (6) **disconnect** removes an isomorphism edge and hence does not alter Grey or Blue.

We use these isomorphisms to show that  $\text{Reach}(\text{Old}_{\text{Start}})$ ,  $\text{Reach}(\text{Old}_{\text{Result}})$  and  $\text{Reach}(\text{New}_{\text{Result}})$  are all isomorphic, thus establishing the correctness of the garbage collector. We remark that verifying the existence of an isomorphism between (subgraphs of) the start graph and the result graph of a graph program execution is not possible with the approach of [13, 14, 12, 15].

A proof tree for the partial correctness for **cheney** is provided in Fig. 6. The assertions in the tree are defined in Definition 8. One of these assertions is  $\text{inv}_{\text{Start}}$  which acts as an invariant of **cheney** (see Lemma 2). We then give arguments about leaves in the proof tree in Lemma 3. The proof tree contains some applications of the proof rule [cons] whose validity is obvious by propositional logic, such as  $c \wedge d \Rightarrow c$ . We do not justify such applications, but we prove in Lemma 4 implications that are not obvious.

**Definition 8.** *Let  $G$  be a graph. We define the following assertions:*

<b>reachOldGrey</b>	: in $\text{Reach}(\text{Old}_G)$ , all nodes are grey
<b>reachOldDashed</b>	: in $\text{Reach}(\text{Old}_G)$ , all edges are dashed
<b>reachNewBlue</b>	: in $\text{Reach}(\text{New}_G)$ , all nodes are blue
<b>nogreynode</b>	: there is no grey node
<b>noblue node</b>	: there is no blue node
<b>nodashededge</b>	: there is no dashed edge
<b>noconnect</b>	: there are no edges between $\text{Old}_G$ and $\text{New}_G$

**inv<sub>Start</sub>** :

- (a) every node in  $G$  is either in  $\text{Old}_G$  or  $\text{New}_G$ , where  $\text{Old}_G$  and  $\text{New}_G$  have the same number of nodes
- (b) there is a unique root node in  $\text{Old}_G$  and at most one root node in  $\text{New}_G$
- (c) each edge in  $G$  is either unmarked or dashed
- (d) all grey nodes are in  $\text{Reach}(\text{Old}_G)$
- (e) all dashed edges are in  $\text{Reach}(\text{Old}_G)$
- (f) all blue nodes are in  $\text{Reach}(\text{New}_G)$
- (g) all green nodes are isolated unlabelled nodes
- (h) there exists an isomorphism  $f: \text{Grey}_G \rightarrow \text{Blue}_G$  where  $\text{Grey}_G$  is the subgraph of  $G$  consisting all grey nodes and all dashed edges between them while  $\text{Blue}_G$  is the subgraph of  $G$  consisting all blue nodes and all edges between them
- (k) each edge  $e$  connecting  $\text{Old}_G$  and  $\text{New}_G$  is an isomorphism edge, that is, an unlabelled and unmarked edge satisfying  $f_V(s_G(e)) = t_G(e)$
- (l)  $\text{Old}_G$  is isomorphic to  $\text{Old}_{\text{Start}}$

**connect** : if  $f_V(v_1) = v_2$  then there exists an edge from  $v_1$  to  $v_2$

**rootOldUnmark** : there exists a unique unmarked root node in  $\text{Old}_G$

**rootOldGrey** : there exists a unique grey root node in  $\text{Old}_G$

**norootNew** : there is no root node in  $\text{New}_G$

**rootNewBlue** : there exists a unique blue root node in  $\text{New}_G$

From now on we denote the pre- and postcondition stated in Section 5 by **precondition** and **postcondition**, respectively.

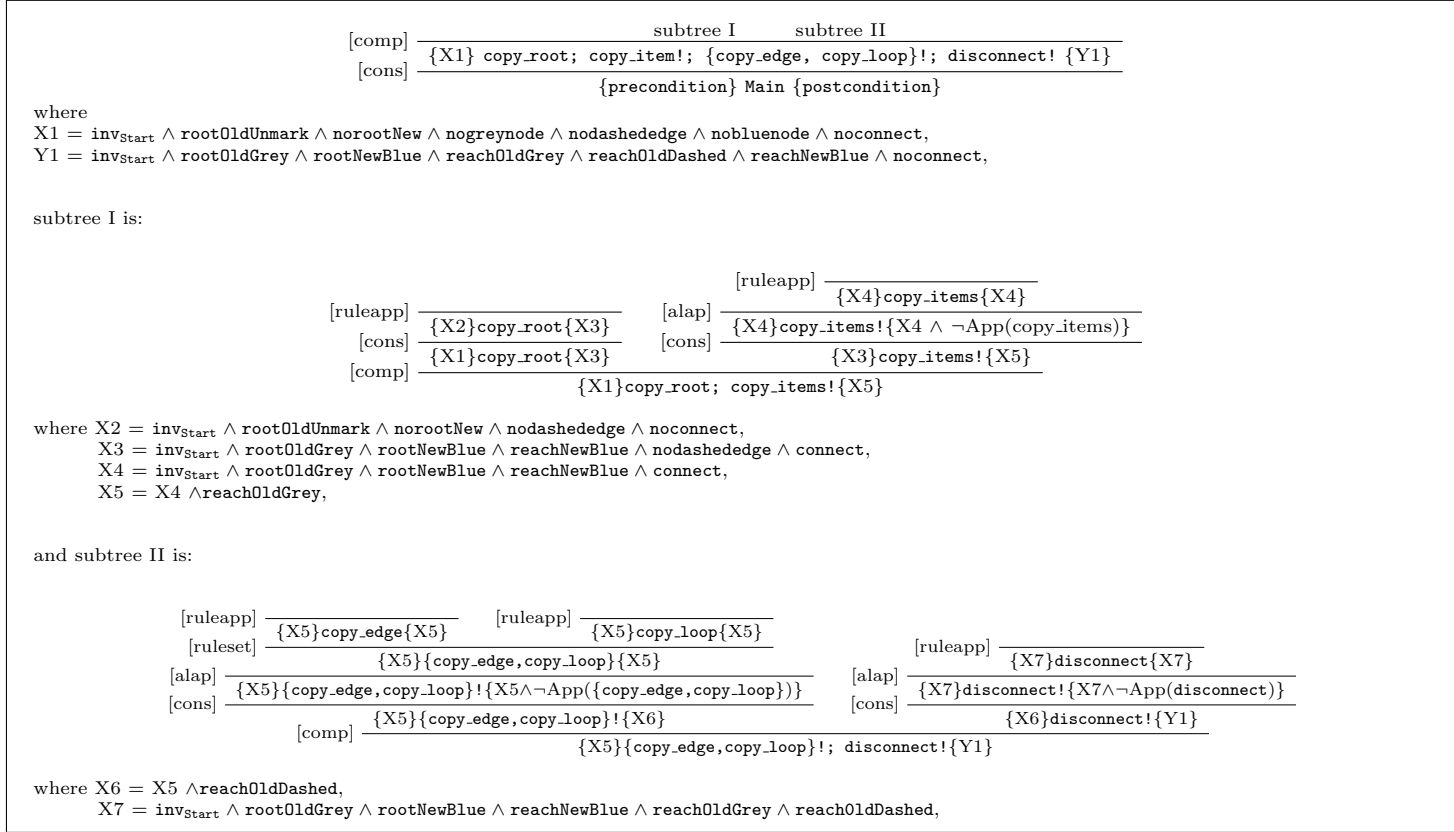
**Lemma 1 (Pre- and postcondition).** *Using the assertions of Definition 8, the following holds:*

**precondition**  $\Leftrightarrow$  **inv<sub>Start</sub>**(a, b, c, g)  $\wedge$  **nogreynode**  $\wedge$  **noblunode**  $\wedge$  **nodashededge**  
 $\wedge$  **rootOldUnmark**  $\wedge$  **norootNew**

**postcondition**  $\Leftrightarrow$  **inv<sub>Start</sub>**(a, b, d, e, f, g, h, l)  $\wedge$  **noconnect**  $\wedge$  **reachOldGrey**  
 $\wedge$  **reachOldDashed**  $\wedge$  **reachNewBlue**

*Proof.* The first sentence of **precondition** is equivalent to **inv<sub>Start</sub>**(a)  $\wedge$  **nogreynode**  $\wedge$  **noblunode**, while the second sentence equivalent to **inv<sub>Start</sub>**(c)  $\wedge$  **nodashededge**. Then, the next sentence is equivalent to **inv<sub>Start</sub>**(g), and the last sentence is equivalent to **inv<sub>Start</sub>**(b)  $\wedge$  **rootOldUnmark**  $\wedge$  **norootNew**.

For **postcondition**, note that we write the result graph as **Result**. The first sentence in **postcondition** is equivalent to **inv<sub>Start</sub>**(a), while the second sentence is equivalent to **noconnect**. Then, the next sentence is equivalent to **inv<sub>Start</sub>**(b)  $\wedge$  **rootOldGrey**  $\wedge$  **rootNewBlue**. With the support of **inv<sub>Start</sub>**(a), the fourth sentence is equivalent to **reachOldGrey**  $\wedge$  **reachOldDashed**  $\wedge$  **inv<sub>Start</sub>**(d)  $\wedge$  **inv<sub>Start</sub>**(e), and the fifth sentences is equivalent to **reachNewBlue**  $\wedge$  **inv<sub>Start</sub>**(f)  $\wedge$  **inv<sub>Start</sub>**(g). The last sentence then equivalent to **inv<sub>Start</sub>**(h)  $\wedge$  **inv<sub>Start</sub>**(l)  $\wedge$  **ReachOldGrey**  $\wedge$  **ReachOldDashed**  $\wedge$  **ReachNewBlue**.  $\square$



**Fig. 6.** Proof tree for partial correctness of `cheney`

**Lemma 2** ( $\text{inv}_{\text{Start}}$  is invariant for all rules). *The following holds:*

$$\begin{array}{ll} \models \{\text{inv}_{\text{Start}}\} \text{copy\_items} \{\text{inv}_{\text{Start}}\} & \models \{\text{inv}_{\text{Start}}\} \text{copy\_loop} \{\text{inv}_{\text{Start}}\} \\ \models \{\text{inv}_{\text{Start}}\} \text{copy\_edge} \{\text{inv}_{\text{Start}}\} & \models \{\text{inv}_{\text{Start}}\} \text{disconnect} \{\text{inv}_{\text{Start}}\} \\ \models \{\text{inv}_{\text{Start}} \wedge \text{norootNew}\} \text{copy\_root} \{\text{inv}_{\text{Start}}\} & \end{array}$$

*Proof.* We proof the lemma by checking each point of  $\text{inv}_{\text{Start}}$ .

(a) The application of any rule in **cheney** does not add or remove any node. It may change the colour of a node from unmarked to grey or from green to blue, which does not change the number of vertices in  $\text{Old}_G$  and  $\text{New}_G$ .

(b) Changes in root node only shown in **copy\_root**, which change unmarked root node to grey root node and add a blue root node. This means the number of root nodes in  $\text{Old}_G$  never changes after any rule application, while the number of root nodes in  $\text{New}_G$  can increase from zero (because of **norootNew**) to one after the application of (**copy\_root**), but never changes after the application of other rules.

(c) Every edge in each rule in the lemma is either unmarked or dashed. Therefore, for all rules in the lemma, since there is no other marks for edges exists before a rule application, they must not exist after the rule application.

(d) We only give the proof for **copy\_items** and **copy\_root** as the other rules do not change grey nodes and the triples must hold. For **copy\_root**, the rule change an unmarked root node to a grey root node. From (b) we know that there is only one root node in  $\text{Old}_G$ , which implies there is no other root node than this new grey root node. The new grey root node is reachable from itself. Then for **copy\_items**, the rule change an unmarked node, which is reachable from a grey node, to grey. Note that the existing grey node is reachable from the root node because  $\text{inv}_{\text{Start}}$  holds before rule application. This means the new grey node is reachable from the root node as well.

(e) We can see that in every rule in the lemma, if there exists a dashed edge then its source and target must be grey. Since grey nodes are in  $\text{Reach}(G)$ , dashed edges must also be in  $\text{Reach}(\text{Old}_G)$ .

(f) Similar to the proof for (d).

(g) There is no rule in the lemma transform an isolated unlabelled green node into a green node that incident to an edge or not unlabelled.

(h) In every rule in the lemma, a production of a new grey node is always accompanied with a production of a new blue node of the same label. A new dashed edge also accompanied with a production of a new edge between blue nodes, and the label of their source node (or target node) is always the same. Then, the new grey node can map to the new blue node, and the new dashed edge can map to the new edge as an addition to the morphism  $f$ . The mappings are isomorphism as they preserve sources, targets, labels, and root.

(k) The edge between Old and New can be inserted only by rules **copy\_root** and **copy\_items**. This means that the edge must be an isomorphism edge.

(l) Every rule in the lemma does not add or delete any unmarked or grey nodes, or edges between them. Modification of these elements exists, where unmarked nodes can transform into grey nodes and unmarked edges can transform

into dashed edges while labels are still preserved. However, the marks indicate no changes in area (New or Old) membership, and there are no changes in adjacency. Hence, sources, targets, and labels are preserved.  $\square$

**Lemma 3.** *For assertions X2, X3, X4, X5, X7 as defined in Fig. 6, the following holds:*

- |  |   |
|--|---|
| (1) $\models \{X2\} \text{ copy\_root } \{X3\}$  | (4) $\models \{X5\} \text{ copy\_loop } \{X5\}$ |
| (2) $\models \{X4\} \text{ copy\_items } \{X4\}$ | (5) $\models \{X7\} \text{ disconnect } \{X7\}$ |
| (3) $\models \{X5\} \text{ copy\_edge } \{X5\}$  |   |

*Proof.* Recall that by Lemma 2, each rule preserves  $\text{inv}_{\text{Start}}$ .

(1) We can see that the application of **copy\_root** preserves the satisfiability of **nodashededge**. The assertion **rootOldUnmark** in the precondition guarantees **rootOldGrey** by the construction of **copy\_root**. Then because the rule creates a new rooted node in the New subgraph, **norootNew** yields **rootNewBlue** after the rule application. **connect** must holds because the new edge must be the only one connecting the two areas as **noconnect** holds in the precondition. It also guarantees **reachNewBlue** because in New subgraph, the new blue root node is an isolated node, so it is the only node that is reachable from the root node.

(2) We can see that **copy\_items** does not change any root, grey, or blue node, so it preserves **rootOldGrey**  $\wedge$  **rootNewBlue** as well. Then, **connect** asserts there exists an edge between  $v_1$  and  $v_2$  for all  $v_1$  and  $v_2$  such that  $f_v(v_1) = v_2$ . From  $\text{inv}_{\text{Start}}$ , we know that  $v_1$  is a grey node and  $v_2$  is a blue node. **copy\_items** yields a new grey and blue node with the same label and there is an edge between them. Hence, the new edge must be an isomorphism edge so that **connect** is preserved. The rule **copy\_items** also changes the number of blue node, so we will need to see how it affects **reachNewBlue**. Follows from point (f) of  $\text{inv}_{\text{Start}}$ , the blue node in the left-hand side of **copy\_items** must be in **Reach(New<sub>G</sub>)**. Therefore, the new blue node in the right-hand side is in **Reach(New<sub>G</sub>)** because the new blue node is reachable from the existing blue node, so that **reachNewBlue** still holds.

(3) Because there are no changes in nodes, **copy\_edge** preserves **rootOldGrey**  $\wedge$  **rootNewBlue**. The adjacency between two grey nodes and between a grey and a blue node are not changed by **copy\_edge**, so **connect**  $\wedge$  **reachOldGrey** still holds. **copy\_edge** adds new edge between two blue nodes. However, since the two are initially are in **Reach(New<sub>G</sub>)** so that it does not change the condition of **ReachNewBlue**.

(4) The rule does not add or remove any node, also any edge between two nodes, so it preserves **rootOldGrey**  $\wedge$  **rootNewBlue**  $\wedge$  **reachNewBlue**  $\wedge$  **reachOldGrey**  $\wedge$  **connect**.

(5) Because **disconnect** does not change any node, also any edge between same-coloured nodes, it preserves **rootOldGrey**  $\wedge$  **rootNewBlue**  $\wedge$  **reachNewBlue**  $\wedge$  **reachOldGrey**  $\wedge$  **reachOldDashed**.  $\square$

**Lemma 4 (Validity of implications).** *For assertions X1, X4, X5, X6, X7, Y1 as defined in Fig. 6, the following holds:*

- |   |  |
|---|--|
| (1) $\text{precondition} \Rightarrow X1$                          | (4) $X5 \wedge \neg \text{App}(\text{copy\_loop}) \Rightarrow X6$  |
| (2) $Y1 \Rightarrow \text{postcondition}$                         | (5) $X4 \wedge \neg \text{App}(\text{copy\_items}) \Rightarrow X5$ |
| (3) $X5 \wedge \neg \text{App}(\text{copy\_edge}) \Rightarrow X6$ | (6) $X7 \wedge \neg \text{App}(\text{disconnect}) \Rightarrow Y1$  |

*Proof.* (1) We will show that **precondition** in Lemma 1 implies X1. In X1, we have additional conjunction point (d), (e), (f), (h), (k), and (l) of  $\text{inv}_{\text{start}}$ . Point (l) is clearly satisfied as **G** is **Start**. Then the other points must hold because **nogreynode**  $\wedge$  **nobluenode**  $\wedge$  **nodashedge** implies the nodes or edges that must satisfies certain requirement do not exist so nothing negate those points. Then, **noconnect** holds because New area only consists of isolated nodes.

(2) By simplification, it is clear that Y1 implies **postcondition**.

(3,4) Similar as above, the non-applicability of **copy\_edge** and **copy\_loop** implies that there is no unmarked edges (including loops) where its source and target is a grey node. Therefore, **reachOldGrey** implies all nodes in  $\text{Reach}(\text{Old}_G)$  are grey and the non-applicability implies that edges between these nodes are not unmarked, i.e. all edges between these nodes are dashed so that **reachOldDashed** holds. Hence, X6 holds.

(5) **connect** and isomorphism in  $\text{inv}_{\text{start}}(h)$  assert that each grey node is a source for an edge where a blue node with the same label as the grey node is the target. **rootOldGrey** implies the existence of grey node.  $\text{inv}_{\text{start}}$  also implies that if there exists an unmarked node, then there must exist an isolated node, as the number of grey and unmarked nodes equal to the number of blue and isolated green nodes while there is a bijective function from grey nodes to blue nodes. Therefore non-applicability of **copy\_items** implies that there is no edge from a grey node to an unmarked node, which means unmarked nodes are not reachable from grey nodes. Then because  $\text{inv}_{\text{start}}$  asserts grey nodes are reachable from the start node in Old area, the unmarked nodes must not reachable from the start node so that **reachOldGrey** holds. Hence, X5 holds.

(6) Non-applicability of **disconnect** implies there are no edges between any grey nodes and any blue nodes.  $\text{inv}_{\text{start}}$  implies that edges connecting  $\text{Old}_G$  and  $\text{New}_G$  are only edges incident to grey and blue nodes. Therefore, the non-applicability implies  $\text{Old}_G$  and  $\text{New}_G$  are not connected which means **noconnect** holds so that Y1 holds.  $\square$

## 6.2 Total Correctness

A graph program  $P$  is totally correct with respect to a precondition  $c$  if the graph program is partially correct, also will not fail or diverge, with respect to  $c$  [14]. We have shown that the program **cheney** is partially correct, so we only need to show that **cheney** cannot fail or diverge.

Let us recall **cheney** at Fig. 4. The command sequence in the program consists of one rule set call and four loop commands. **Precondition** clearly stated the existence of an unmarked root node. Then, because **precondition** guarantees

the same number of unmarked and green nodes, there must exist at least one green node as we have an unmarked root node. Therefore, the match of the left-hand side of `copy_root` is guaranteed by `precondition` so it will not fail. By the semantic of loop command, the other commands in the sequence will not fail either so that the absence of failure in the execution of `cheney` is guaranteed.

The rule `copy_root` is only applied once, so it will not diverge. For loop commands, elimination of an element is clearly can guarantee the absence of divergence. The rule `copy_items`, `copy_edge`, `copy_loop`, and `disconnect` eliminate unmarked nodes, unmarked edges between two grey nodes, unmarked loops on grey nodes, and edges between grey and blue nodes respectively.

## 7 Related Work

In this study, we implement Cheney’s copying garbage collector [3] in GP 2 and reason about it. Several works in verification of Cheney’s algorithm stated the difficulties in verifying the algorithm, such as in reasoning about reachability in graphs [8] and verifying programs involving cyclic data structures.

Torp-Smith et.al. [16] is the extended version of [2]. In the study, they extend standard separation logic so that it can be used in cyclic data structures. Some remarks are stated to show the advantage in using local reasoning for the verification, such as ensuring an assignment is not affecting some assertions. The isomorphism between data structures from two different points in time; before and after a program execution; is also introduced in the study. Again, separating conjunction is used to reason about isomorphism as the update of bijective function can be seen in local reasoning. However, we think their proof is complicated as there are so many rules involved yet the validity of the rules is not provided. Moreover, there are 57 pages in the journal paper to discuss the verification of Cheney’s garbage collector. They use about 48 triples in the proof, but reasoning about each step takes a lot of work as well.

In McCreight’s PhD thesis [7], they use the definition of a morphism from [16] and study about mechanised verification of Cheney’s algorithm. They also use separation logic for their verification. Their proof is detailed as they see all possible cases to mechanise the verification, but they use various lemmas, and it is not clear how the lemmas are proved. The proof is separated into five parts. The verification of each part requires between one and seven pages. However, to fully understand the verification, we need to understand the specification of each part which is not more concise than the verification itself. In total, there are about 50 pages of the thesis that deal with Cheney’s algorithm.

This may result from the level difference in the language, as they use low-level programming and we use a programming language that abstracts from details of memory management such as address arithmetic. The other studies use separation logic for local reasoning in the verification. However, because sharing mostly exists in graph problems including garbage collector, they need to extend separation logic so that it can be used in the verification of Cheney’s algorithm. The extension itself is not easy, and proofs following this extension

is complex. In contrast, there is no need for us to extend the existing proof calculus for reasoning about our graph program. By using the existing proof calculus, which is sound in the sense of partial correctness, we are able to show verification of **cheney** in a simple way with clear justification for each step of verification.

Another extension of separation logic for the verification of Cheney’s garbage collector can be found in [5]. The paper introduces the notion of sharing in separation logic, which is called ramification. This allows local reasoning while global effects are still accounted for when they are required, enabling reasoning about programs that manipulate data structures with unrestricted sharing. Different from [2, 16], the paper uses inductive graph predicates and does the reasoning on the level of mathematical graphs. It is claimed that the verification is more concise than in other work. This is indeed the case as there are only about two pages to discuss Cheney’s garbage collector and its verification. However, we find it difficult to see the reasoning about implications given in the proof. In addition, one needs to understand the intricate theory about ramification and its use in verification.

## 8 Conclusion and Future Work

We have implemented Cheney’s copying garbage collector in GP 2 and verified the program using Hoare logic. To be compared to the previous work we described in the previous section, they use local reasoning and argue that this helps them so that their reasoning is less complicated [16]. To be compared with our work, the use of marks in our program implicitly helps us in separating properties that are not affected as we can focus on structures with specific marks. Similarly, the update of bijective function in our case can be seen with the use of marks.

We show a proof tree for the partial correctness of the program, and only from this, we argue that the proof is more straightforward than other proofs that have been done for the Cheney’s algorithm. Moreover, in our work we use proof rules that are proven to be sound to connect one triple to another. But in other literature about verification of Cheney’s algorithm, they do not have a clear reasoning about soundness of proof rules they use. Moreover, from the proof sketch, we only use 22 triples while the proof sketch in [16] has about 48 triples. This shows how concise our proof if we compare to theirs.

Our proof is more concise than others partly because we still use arbitrary mathematical language for the assertions, unlike others that have defined formal assertions for this. Although there are E-conditions [12] that can be used to express properties of graphs in graph programs, we still need to extend this. We need an assertion that can describe a condition between two graphs that exist at a different point in time, but E-condition we have now only expressed the graph that exists at one point in time. Moreover, E-condition that based on first-order logic is not enough to represent properties we need, e.g. the existence of two area Old and New. M-condition [15] can cover this, but the formal definition of this is



yet to be defined. However, none of these can be used to express the isomorphism between two graphs that exist in different time. In the future, we plan to look the transduction in monadic second-order logic [4] and have assertion language that can express isomorphism between two structures.

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